AMENDMENTS TO THE CLAIMS

Claim 16 was pending in the Application. Claim 16 is an independent claim and claims 17-21 depend therefrom. Claim 22 is an independent claim and claims 23-27 depend therefrom. Claim 16 is currently amended. Claims 17-27 are new. Claims 1-15 were previously canceled.

Listing of Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Claims 1-15. (Canceled)

Claim 16. (Currently Amended)   A system for providing video, said system comprising:

a video decoder, said video decoder further comprising:

a decompression engine for decompressing compressed video data, there[[ ]]by resulting in decompressed video data; and

a deinterlacer for deinterlacing the decompressed video data, thereby resulting in deinterlaced video data; and

a display engine, said display engine comprising:

a scalar for scaling the deinterlaced video data[[.]]:

wherein the video decoder and the display engine are discrete components.

Claim 17. (New)   The system of claim 16, wherein the video decoder is an MPEG-2 video decoder.

Claim 18. (New)   The system of claim 16, wherein the decompression engine performs lossy decompression.

Claim 19. (New)   The system of claim 18, wherein the lossy decompression further comprises motion compensation.

Claim 20. (New)   The system of claim 18, wherein the decompression engine also performs lossless decompression.

Claim 21. (New)   The system of claim 20, wherein lossless decompression comprises Huffman Decoding.

Claim 22. (New)   A circuit for providing video, said circuit comprising:

a video decoder, said video decoder further comprising:

a decompression engine operable to decompress compressed video data, thereby resulting in decompressed video data; and

a deinterlacer connected to the decompression engine, the deinterlacer operable to deinterlace the decompressed video data, thereby resulting in deinterlaced video data; and

a display engine connected to the video decoder, said display engine comprising:

a scalar operable to scale the deinterlaced video data;

wherein the video decoder and the display engine are discrete components.

Claim 23. (New)   The circuit of claim 22, wherein the video decoder is an MPEG-2 video decoder.

Claim 24. (New)   The circuit of claim 22, wherein the decompression engine performs lossy decompression.

Claim 25. (New)   The circuit of claim 24, wherein the lossy decompression further comprises motion compensation.

Claim 26. (New)   The circuit of claim 24, wherein the decompression engine also performs lossless decompression.

Claim 27. (New)   The circuit of claim 26, wherein lossless decompression comprises Huffman Decoding.